

1/

FIG. 2

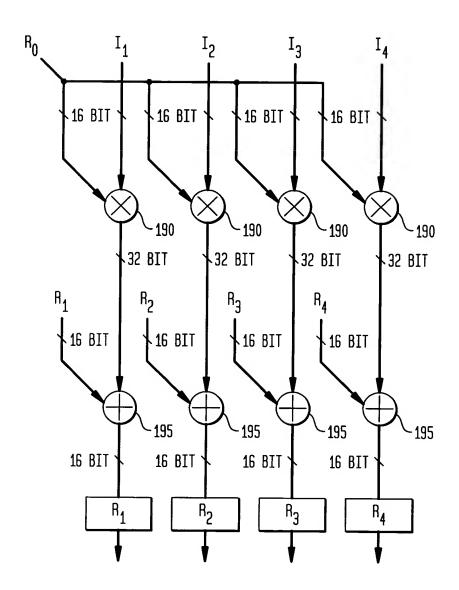


FIG. 3

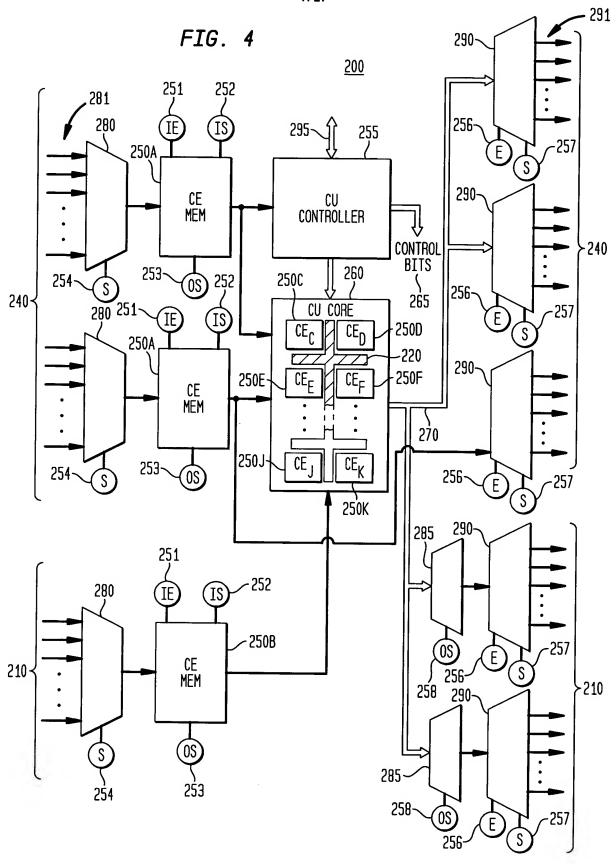


FIG. 5A

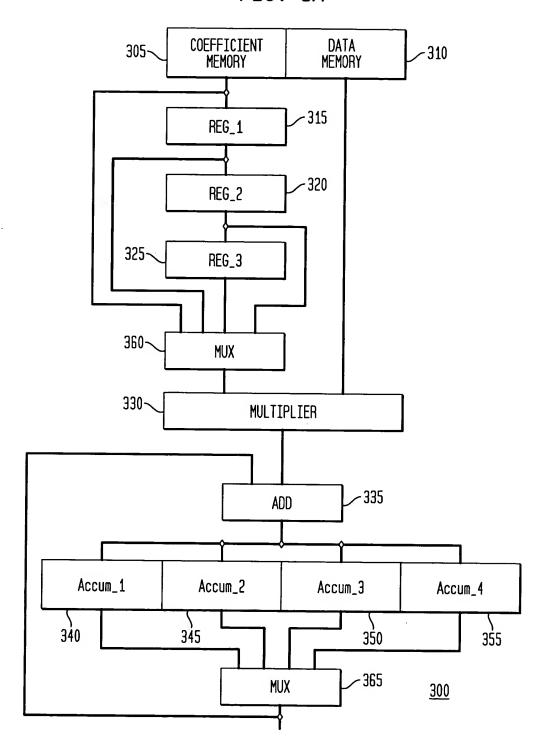
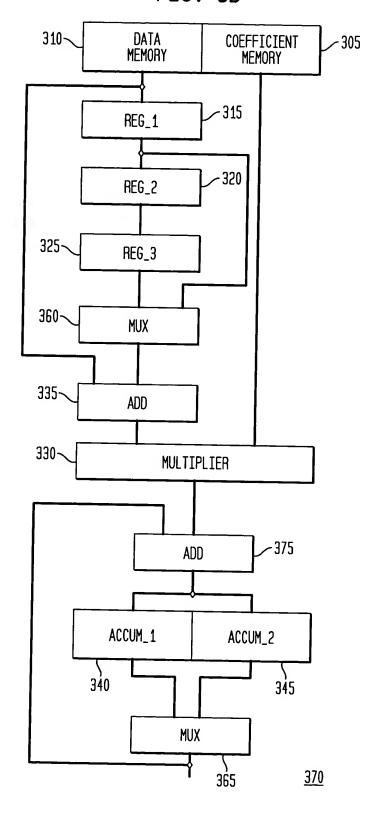


FIG. 5B



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FIG. 5C

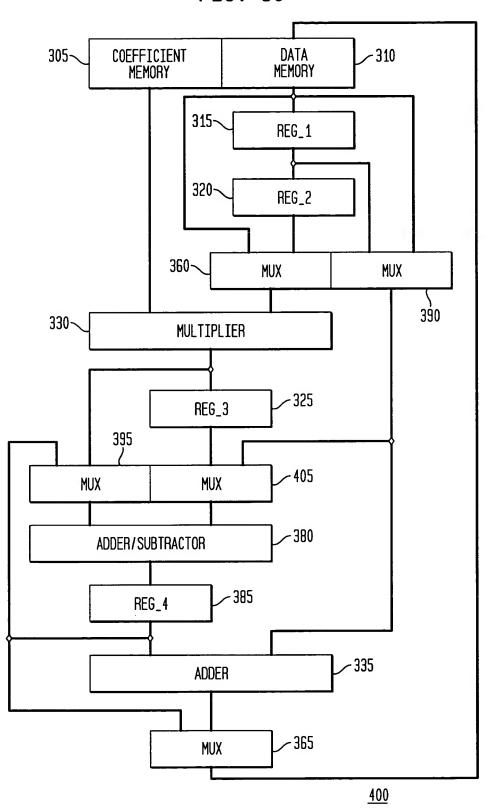
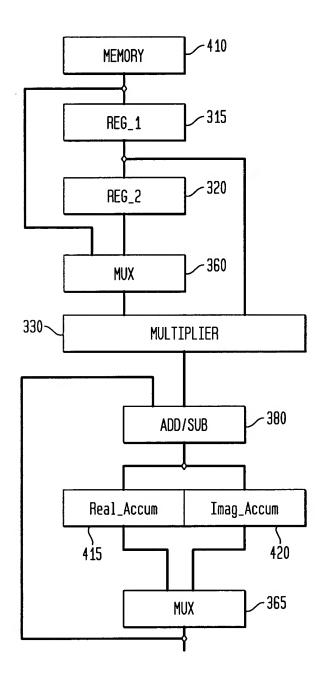


FIG. 5D



<u>440</u>

9/27 FIG. 5E IN-OUT <u>460</u> Rtop OUT <-- IN + Rtop Rbot <-- IN * b₂ + OUT * a₂ Rtop <-- IN * b₁ + OUT * a₁ + Rbot Z⁻¹ Rbot **√305** 490-MEMORY (IN) COEFF. -360 MUX **330** MULTIPLIER **390** MUX **335** ADD - 395 MUX 475 / 485 480 **REG-ACC** REG-TOP **REG-OUT** REG-BOT 470 **√365** MUX OUT <u>450</u>

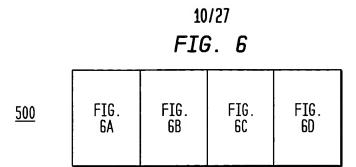
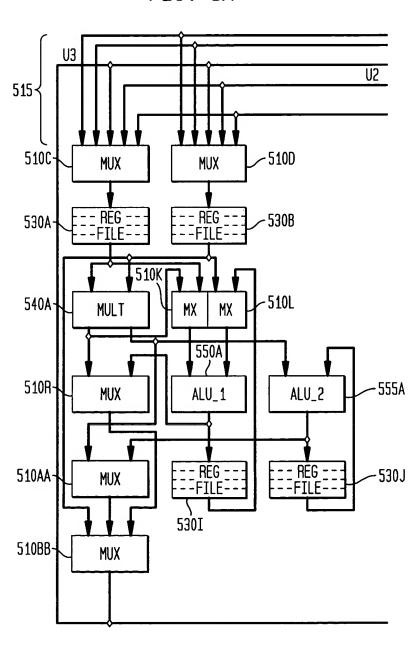
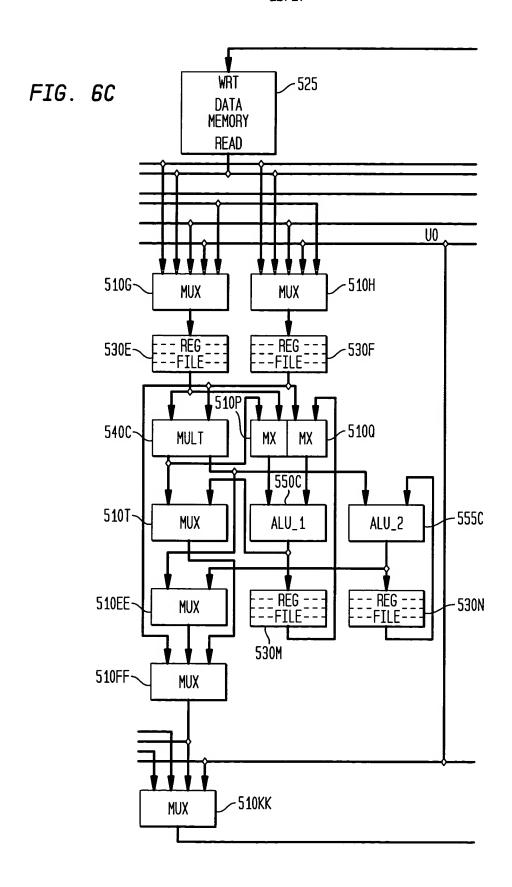
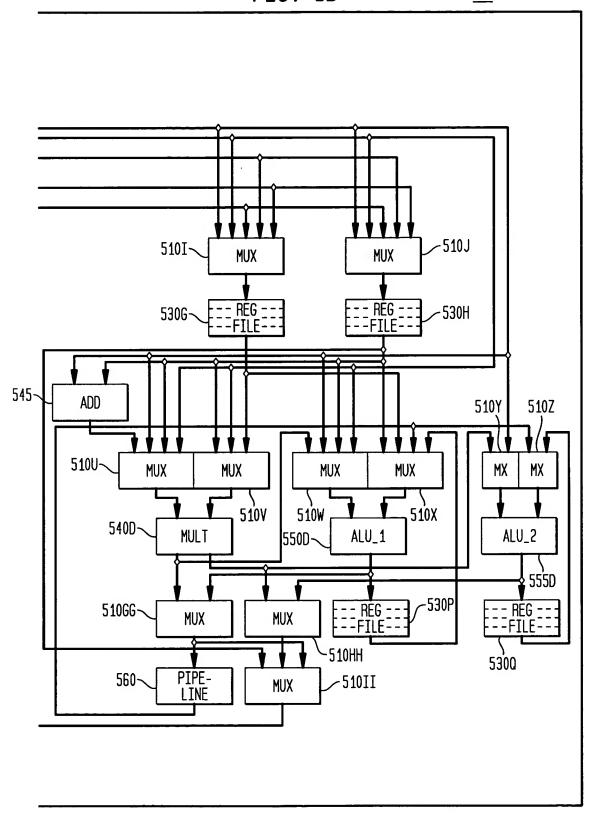
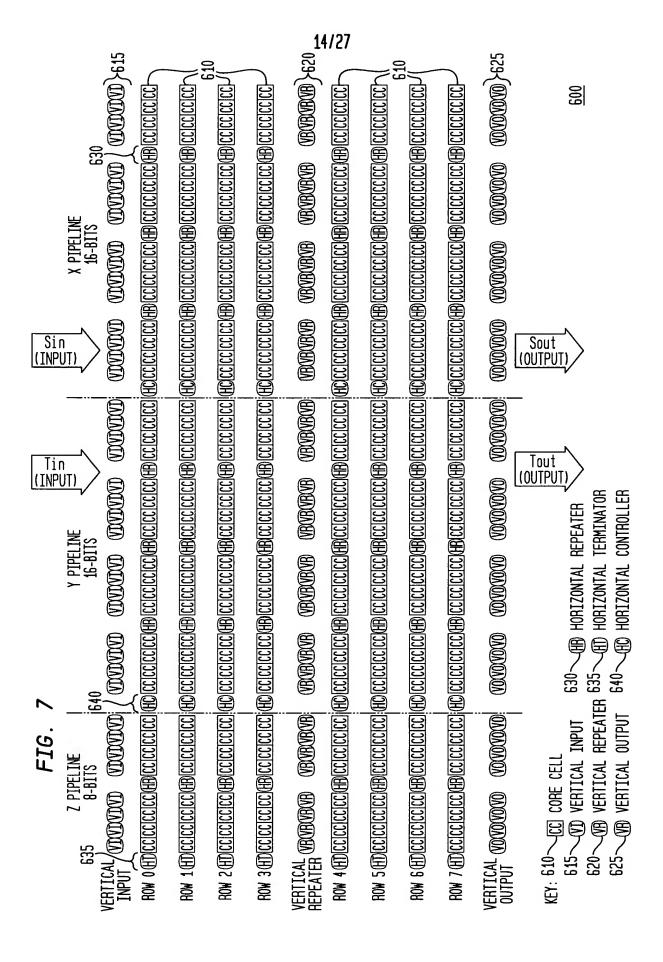


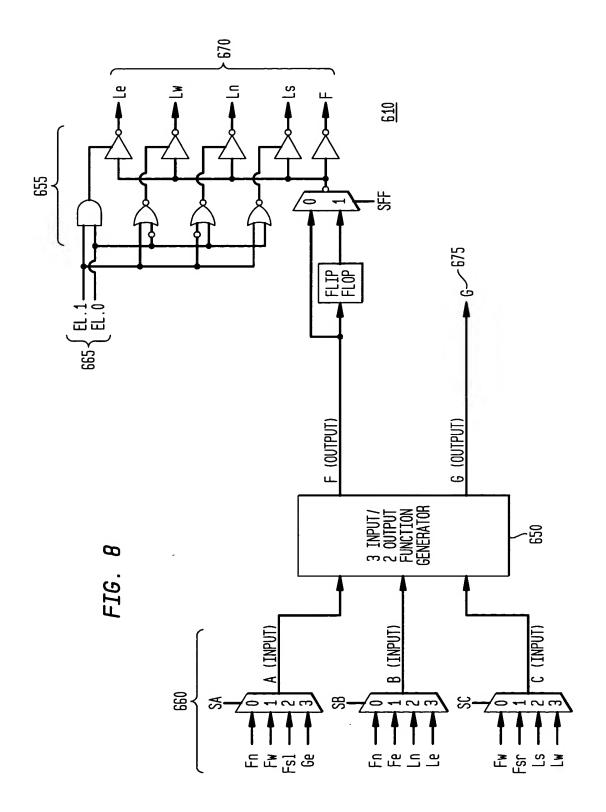
FIG. 6A











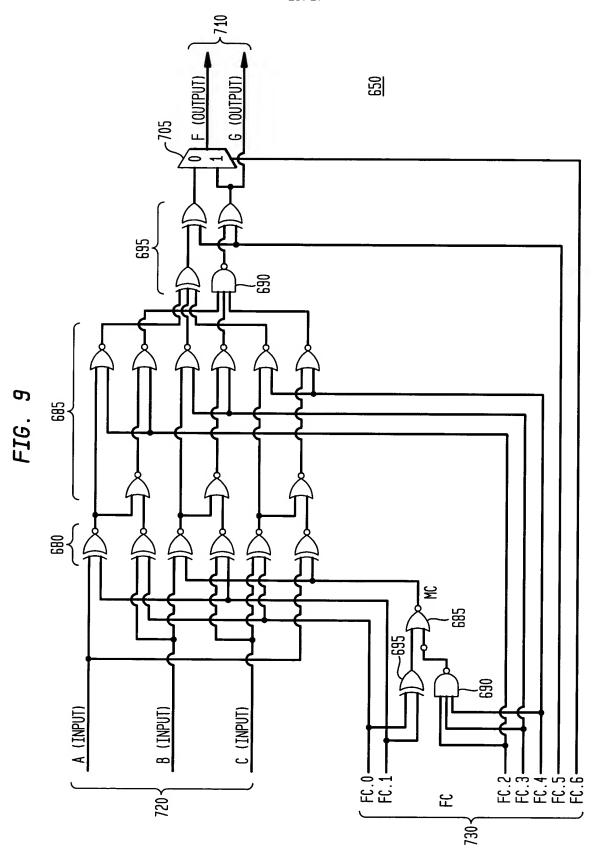


FIG. 10

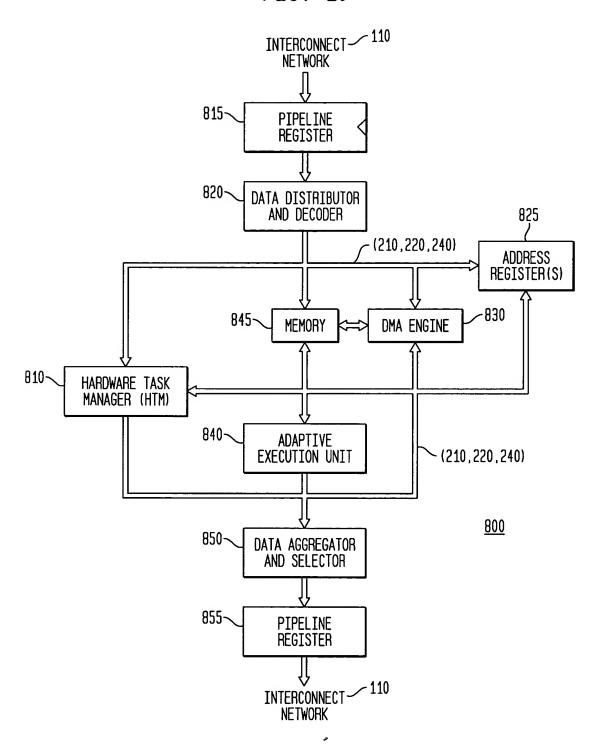


FIG. 11

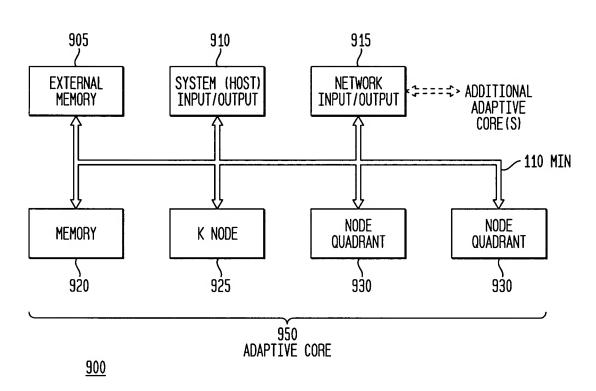
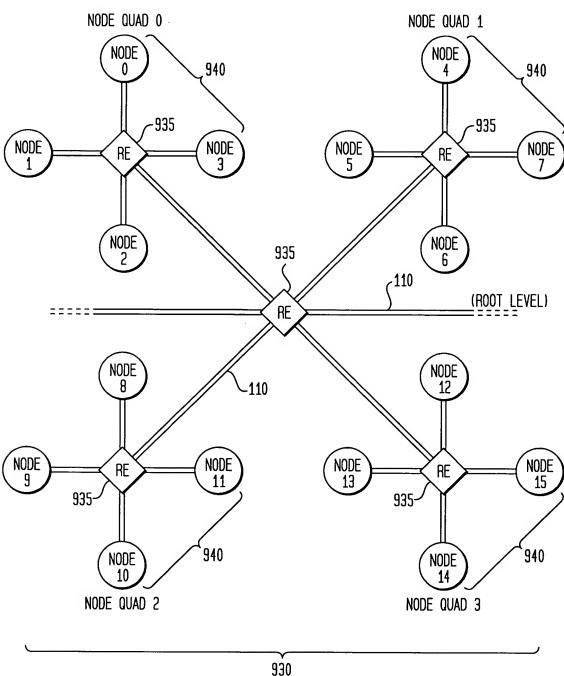
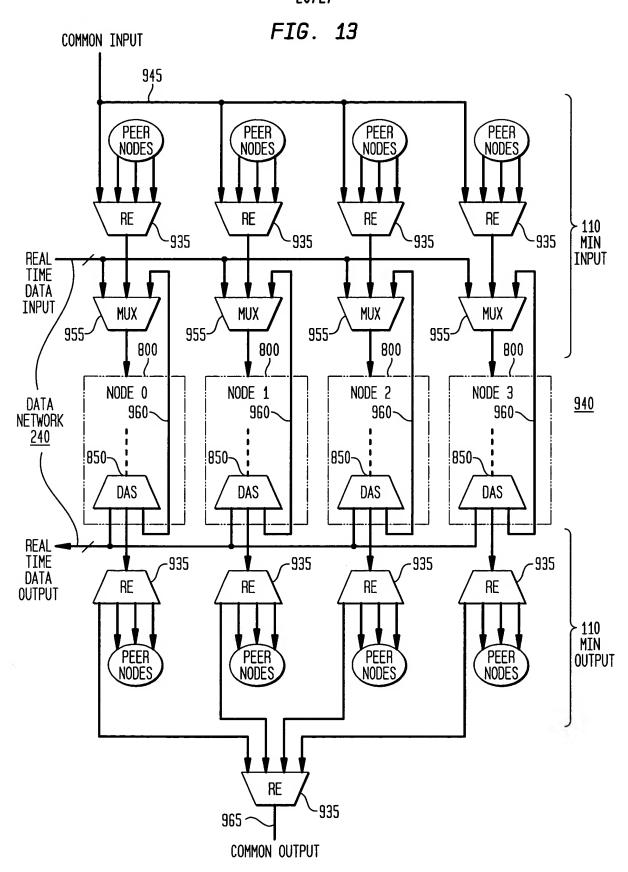


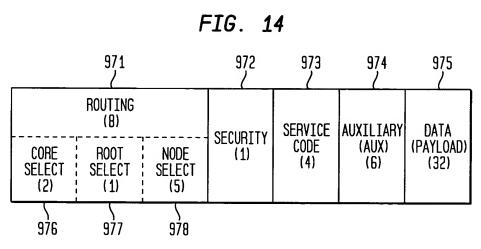
FIG. 12



930 NODE QUADRANT

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<u>970</u>

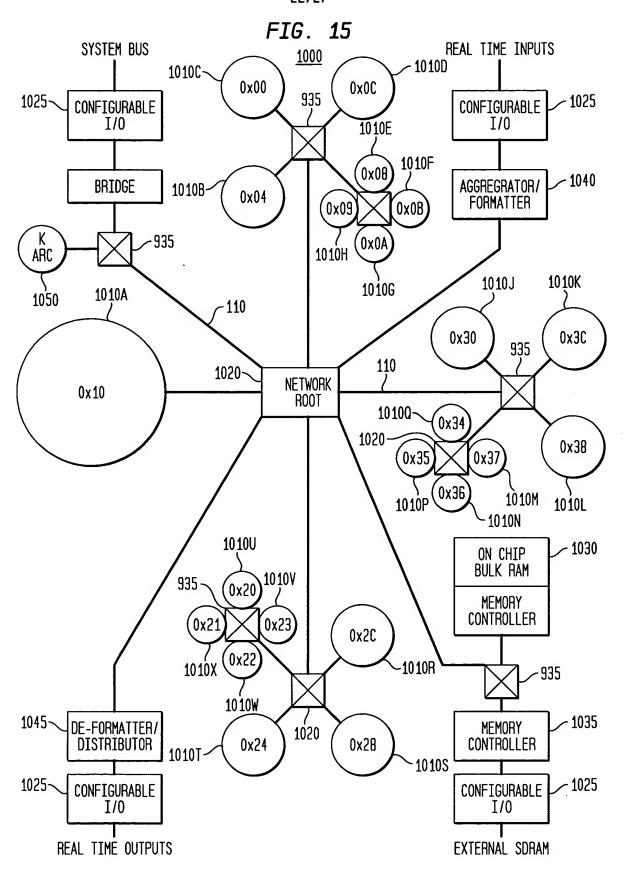
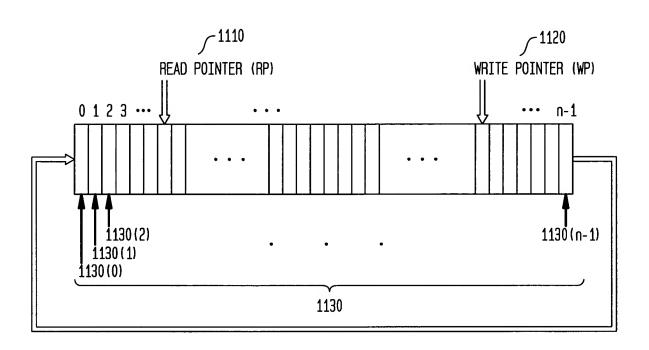
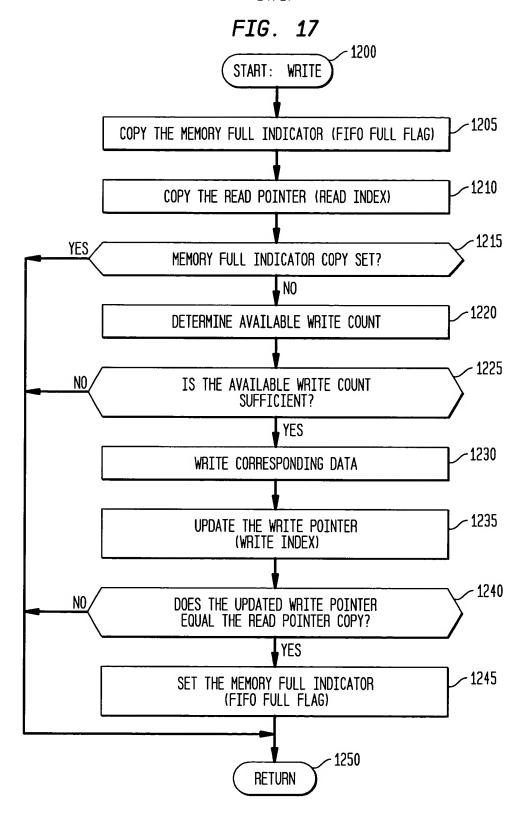
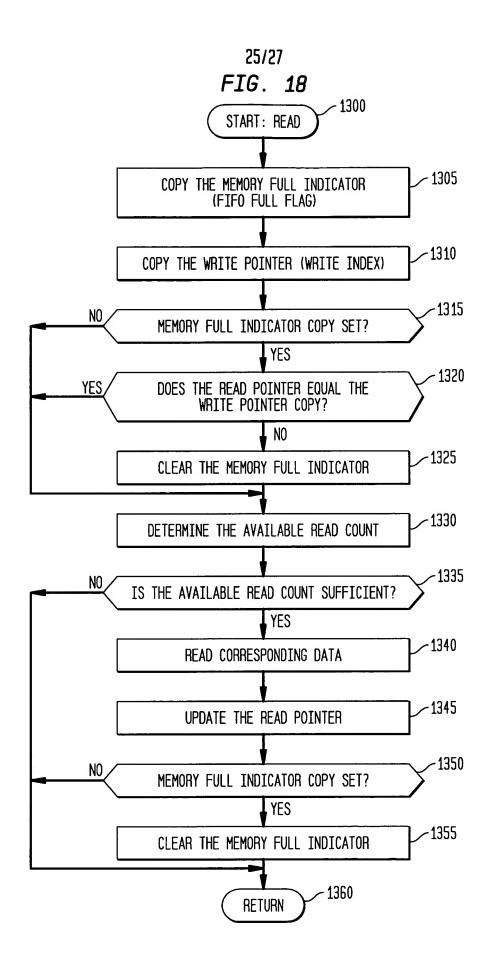


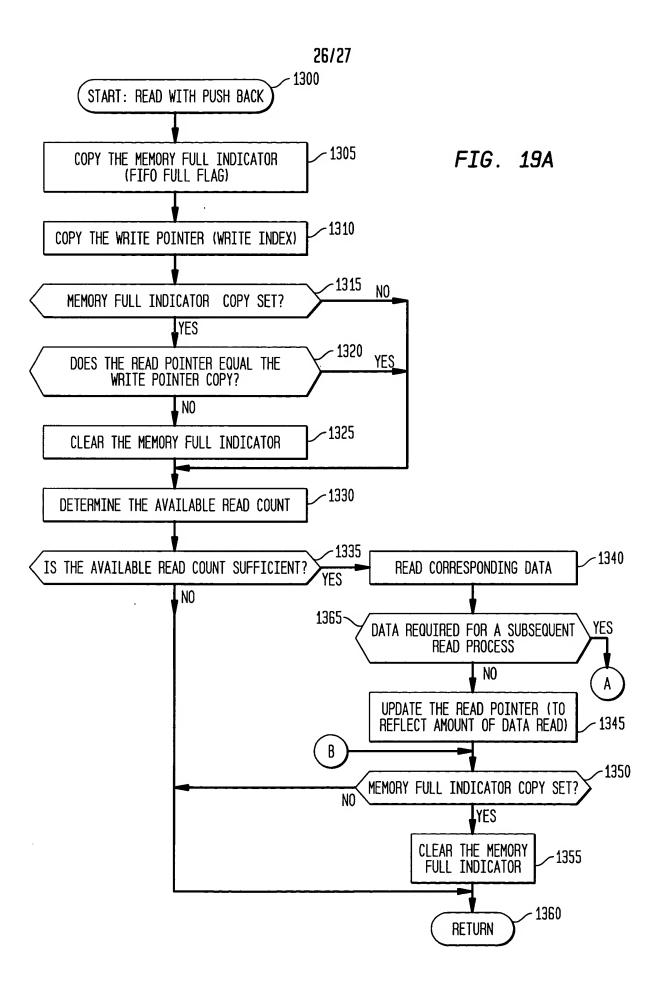
FIG. 16



<u>1100</u>







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